

# GURU NANAK INSTITUTIONS TECHNICAL CAMPUS

(An UGC Autonomous Institution - Affiliated to JNTUH)

Ibrahimpattanam, Ranga Reddy (District), Hyderabad - 501 506.



## Department of Electrical & Electronics Engineering

### Regulation - R22

### COURSE STRUCTURE

(Applicable from the batch admitted during 2022-23 onwards)

#### I- Year Semester -1

S. No	Course Code	Course Title	Hours per Week			Credits
			L	T	P	
1		Matrices and Calculus	3	1	0	4
2		Engineering Chemistry	3	1	0	4
3		C Programming and Data Structures	3	0	0	3
4		Electrical Circuit Analysis – I	3	0	0	3
5		Computer Aided Engineering Graphics	1	0	4	3
6		Elements of Electrical and Electronics Engineering	0	0	2	1
7		Engineering Chemistry Laboratory	0	0	2	1
8		C Programming and Data Structures Laboratory	0	0	2	1
<b>Total</b>			<b>13</b>	<b>2</b>	<b>10</b>	<b>20</b>

Dr. K. Santhi  
Chairman, BOS  
EEE

Dr. K. Naga Sujatha  
JNTUH Nominee  
EEE

Dr. E. Vidya Sagar  
Academic Council Nominee  
EEE

Dr. P.V. Prasad  
Academic Council  
Nominee  
EEE

Mr. S. V. Raman  
Industry Representative  
EEE

Dr. A. Damodaram  
JNTUH Nominee  
CSE(AIML,AIDS,IOT,CS&DS)

Dr. G. V. Narsimha Reddy  
JNTUH Nominee  
CE

Dr. B. Kranthi Kiran  
JNTUH Nominee  
CSE/IT

Dr. Ch. Sridhar Reddy  
JNTUH Nominee  
ME

Dr. P. Chandra Sekhar Reddy  
JNTUH Nominee  
ECE

Dr. B. Sathyanarayana  
JNTUH Nominee  
Chemistry

Dr. T. Srikanth  
JNTUH Nominee  
Physics

Dr. V. Srinivasa Kumar  
JNTUH Nominee  
Mathematics

Dr. V. Parvathy  
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English

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#### I -Year Semester -2

S. No	Course Code	Course Title	Hours per Week			Credits
			L	T	P	
1		Ordinary Differential Equations and Vector Calculus	3	1	0	4
2		Applied Physics	3	1	0	4
3		Engineering Workshop	0	1	3	2.5
4		English for Skill Enhancement	2	0	0	2
5		Electrical Circuit Analysis - II	2	0	0	2
6		Applied Physics Laboratory	0	0	3	1.5
7		English Language and Communication Skills Laboratory	0	0	2	1
8		Applied Python Programming Laboratory	0	1	2	2
9		Electrical Circuit Analysis Laboratory	0	0	2	1
Total			10	4	12	20

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